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		Application Number	10/823/15 10/823/15		
		Filing Date	4/12/04		
		First Named Inventor	Woo		
		Group Art Unit	2188		
		Examiner Name	Verbrugge		
Sheet	2	of	2	Attorney Docket Number	RB1-029USC100

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
W		"Power Aware Page Allocation", Lebeck et al., ACM, November 2000, 12 pages.	
W		"Optimizing the DRAM Refresh Count for Merged DRAM/Logic LSIs", Ohsawa et al., ISLPED, August 10-12, 1998, pages 82-87.	

Examiner Signature	Kevin Verbrugge	Date Considered	9/2/04
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹ Unique citation designation number. ² Applicant is to place a check mark here if English language Translation is attached.

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